

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10791337			
<b>Filing Date:</b>	01-Mar-2004			
<b>Title of Invention:</b>	Spacer-less transistor integration scheme for high-k gate dielectrics and small gate-to-gate spaces applicable to Si, SiGe strained silicon schemes			
<b>First Named Inventor/Applicant Name:</b>	David Pritchard			
<b>Filer:</b>	Francis Theodor Kalinski II/Lydie Fitzsimmons			
<b>Attorney Docket Number:</b>	03-2051/LSI1P240			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				120